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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/926,447	10/21/2002	Thomas Grassl	GRAS3004/JEK	4692
23364 PACON & TH	7590 06/01/2007		EXAMINER	
BACON & THOMAS, PLLC 625 SLATERS LANE FOURTH FLOOR ALEXANDRIA, VA 22314			SELLS, JAMES D	
			. ART UNIT	PAPER NUMBER
1	,		1734	
			MAIL DATE	DELIVERY MODE
			06/01/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)
	09/926,447	GRASSL ET AL.
Office Action Summary	Examiner	Art Unit
	James Sells	1734
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the o	correspondence address
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tin will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).
Status		
1)⊠ Responsive to communication(s) filed on <u>28 Fe</u> 2a)□ This action is FINAL . 2b)⊠ This 3)□ Since this application is in condition for allowant closed in accordance with the practice under E	action is non-final. ace except for formal matters, pro	
Disposition of Claims		
4) Claim(s) 18,20-22,27,30-33,35-39,42-45 and 49 4a) Of the above claim(s) is/are withdraw 5) Claim(s) is/are allowed. 6) Claim(s) 18,20-22,27,30-33,35-39,42-45 and 49 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or	vn from consideration. <u>9-51</u> is/are rejected.	ation.
Application Papers		•
9) The specification is objected to by the Examiner 10) The drawing(s) filed on is/are: a) access Applicant may not request that any objection to the of Replacement drawing sheet(s) including the correction 11) The oath or declaration is objected to by the Examiner	epted or b) objected to by the I drawing(s) be held in abeyance. See on is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau * See the attached detailed Office action for a list of	s have been received. s have been received in Applicati ity documents have been receive (PCT Rule 17.2(a)).	on No ed in this National Stage
Attachment(s) 1) ☑ Notice of References Cited (PTO-892)	4) ☐ Interview Summary	(PTO-413)
2) Notice of Preferences Cited (* 10-032) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	ate

DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 18, 27, 33 and 39 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lang et al (US Patent 6,046,073) in view of Grupen-Shemansky (US Patent 5,268,065) in further view of Gloton et al (US Patent 5,569,879).

Lang discloses a process for making semiconductor chips and the chips made thereby. As shown in Fig. 1, a thinned semiconductor chip 1 is arranged on contact surfaces 2 of a leadframe. Electrical connections are formed between contact studs 3 and contact surfaces 2. See col. 2, line 63 through col. 3, line 30. At col. 2, lines 1-15, Lang discloses that the assembly is in normal use in a smart card assembly. In addition, it appears from Fig. 1, that chip 1 is applied to the external surface of the card in the manner claimed by the applicant.

However, Lang does not disclose thinning the chip from the back side as claimed by the applicant. Regarding this difference, the applicant is directed to the reference of Grupen-Shemansky.

Grupen-Shemansky discloses a method for thinning a semiconductor wafer. As shown in the figures, back side 13 of semiconductor wafer 11 undergoes a mechanical grinding in order to thin the chip. See col. 2, line 56 through col. 3, line 3.

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It would have been obvious to one having ordinary skill in the art to thin a chip from the back side, as taught by Grupen-Shemansky, in the process of Lang, in order to provide smaller, more compact chips.

However, Lang does not disclose the chip having an active front side as claimed by the applicant. Regarding this difference, the applicant is directed to the reference of Gloton et al.

Gloton discloses a chip card or smart card construction. As shown in Fig. 8, the card comprises chip 100, which is applied to the surface of the smart card. Conductors 103 connect the active front side of the chip to patterned strip 10.

It would have been obvious to one having ordinary skill in the art to employ a chip with an active front side, as taught by Gloton, in the process of Lang, in order to provide easier access to the active components in the chip.

3. Claims 20-22, 30-32, 35-38 and 42-45 and 49-51 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lang et al in view of Grupen-Shemansky and Gloton et al as described above in paragraph 2 in view of Kohama et al (US Patent 6,412,701).

Kohama discloses a method for incorporating a chip into a smart card. This method involves applying the chip to a surface of the smart card externally. As shown in Figs. 8-9, chip 1 with conductive paths or coils 2 is "hot pressed" in recesses 15 with its front side pointing outside to the surface of the smart card. See col. 12, lines 4-18 and 46-51.

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It would have been obvious to one having ordinary skill in the art to hot press the chip into a cavity, as taught by Kohama, in the method and article of Lang in order to produce smart cards with desired physical configurations. In addition, without the disclosure of unexpected results, it is the examiner's position that the protective lacquer and printing technique are well known and conventional in the art and would have been obvious to employ in the method and article of Lang as described above in order to provide desired protection and to facilitate production of the articles.

Response to Arguments

4. Applicant's arguments with respect to claims 18, 27, 33 and 39 have been considered but are moot in view of the new ground(s) of rejection.

Telephone/Fax

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to James Sells whose telephone number is (571) 272-1237. The examiner can normally be reached on Monday-Friday between 9:30 AM and 6:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chris Fiorilla can be reached at (571) 272-1187. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

JAMES SELLS Primary Examiner Tech. Center 1700